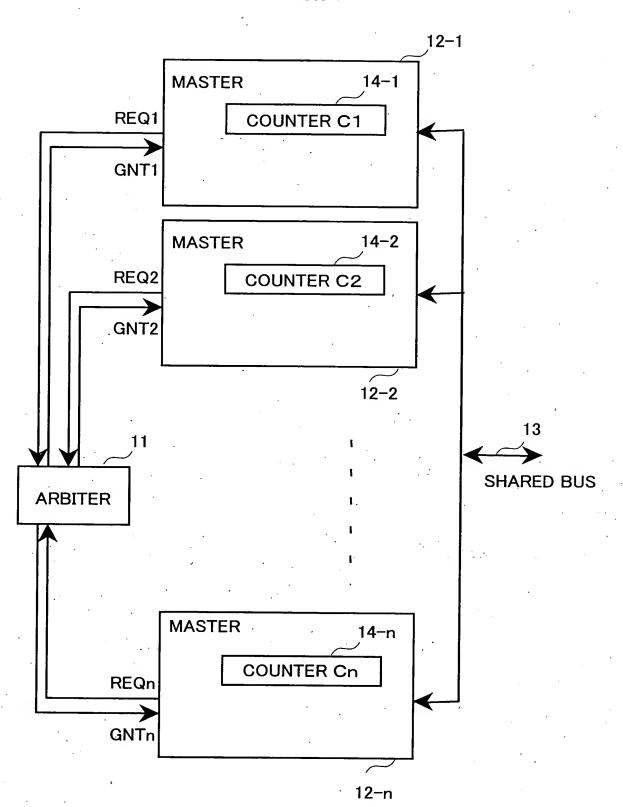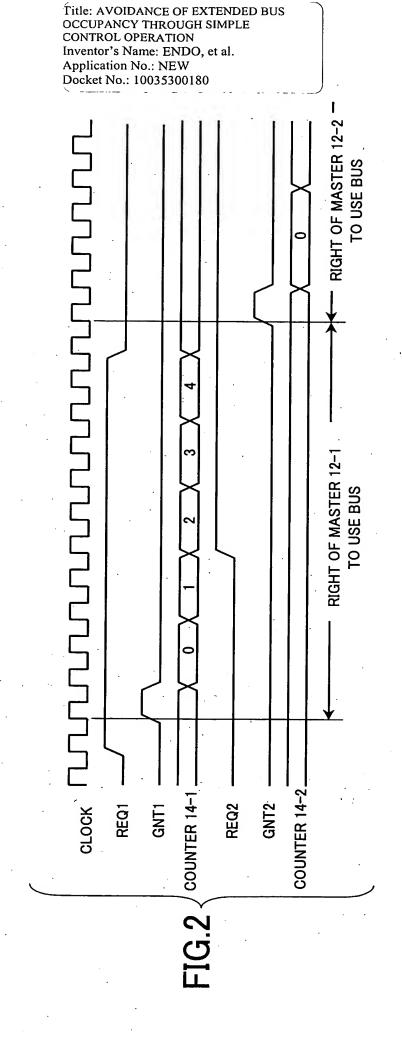# FIG.1

Title: AVOIDANCE OF EXTENDED BUS
OCCUPANCY THROUGH SIMPLE
CONTROL OPERATION
Inventor's Name: ENDO, et al.
Application No.: NEW
Docket No.: 10035300180

FIG.2

Title: AVOIDANCE OF EXTENDED BUS
OCCUPANCY THROUGH SIMPLE
CONTROL OPERATION
Inventor's Name: ENDO, et al.
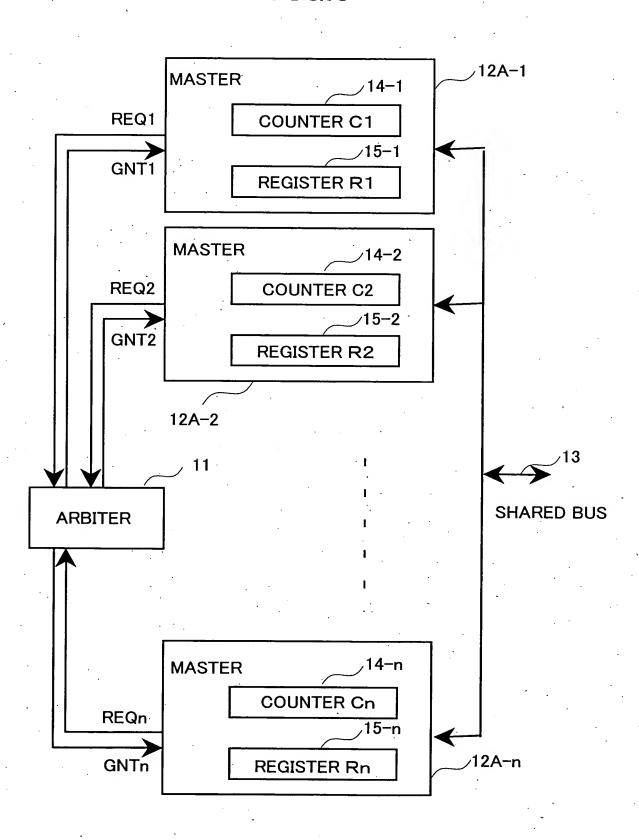Application No.: NEW
Docket No.: 10035300180

FIG.3

Title: AVOIDANCE OF EXTENDED BUS
OCCUPANCY THROUGH SIMPLE
CONTROL OPERATION
Inventor's Name: ENDO, et al.
Application No.: NEW
Docket No.: 10035300180

FIG.4

Title: AVOIDANCE OF EXTENDED BUS
OCCUPANCY THROUGH SIMPLE
CONTROL OPERATION
Inventor's Name: ENDO, et al.
Application No.: NEW
Docket No.: 10035300180
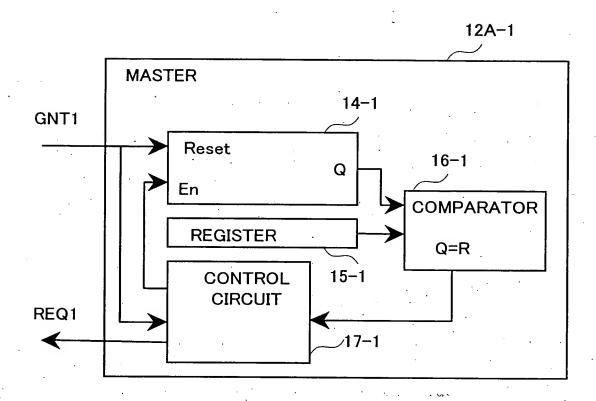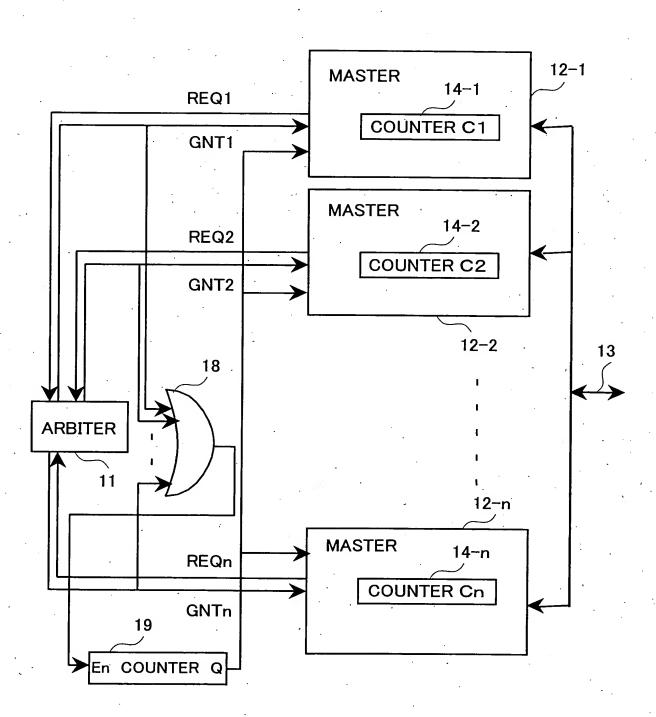
FIG.5

Title: AVOIDANCE OF EXTENDED BUS
OCCUPANCY THROUGH SIMPLE
CONTROL OPERATION
Inventor's Name: ENDO, et al.
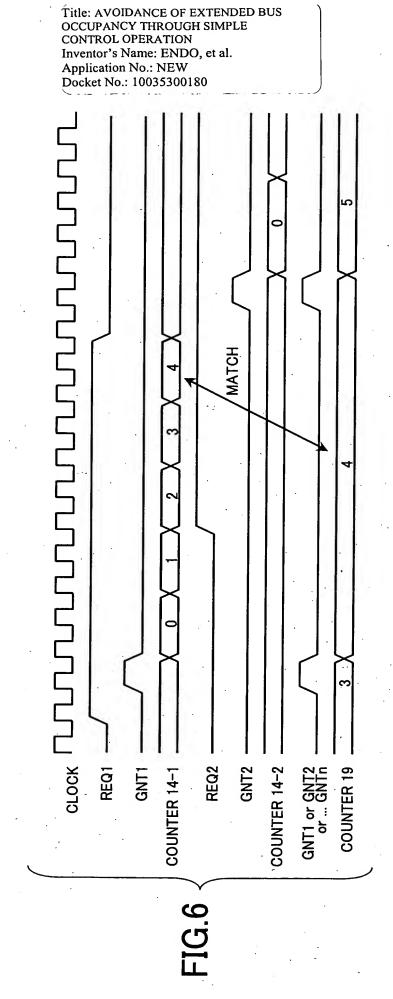Application No.: NEW
Docket No.: 10035300180
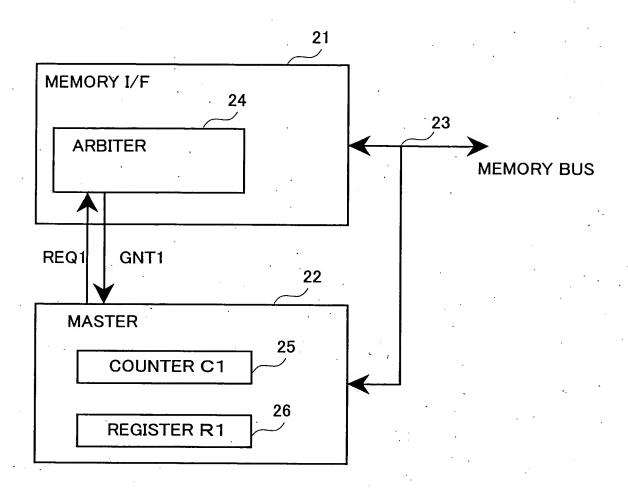
FIG.6

Title: AVOIDANCE OF EXTENDED BUS
OCCUPANCY THROUGH SIMPLE
CONTROL OPERATION
Inventor's Name: ENDO, et al.
Application No.: NEW
Docket No.: 10035300180

FIG.7

FIG.8

21A

MEMORY INTERFACE

24A

ARBITER

COUNTER Ca — 31

REGISTER Ra — 32

23

MEMORY BUS

REQ1   GNT1

22

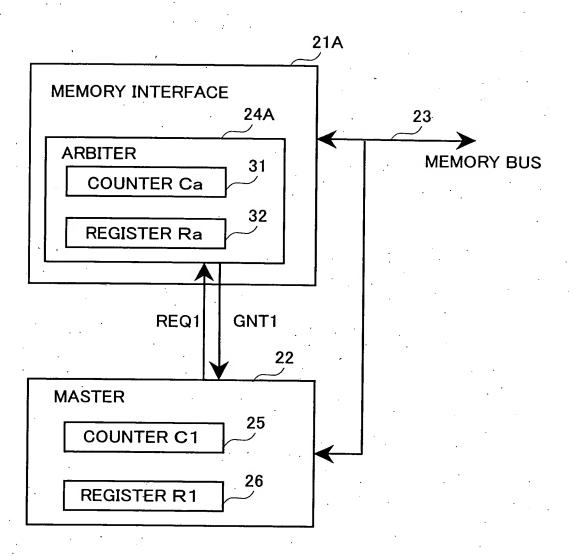**MASTER**

COUNTER C1 — 25

REGISTER R1 — 26

Title: AVOIDANCE OF EXTENDED BUS
OCCUPANCY THROUGH SIMPLE
CONTROL OPERATION
Inventor's Name: ENDO, et al.
Application No.: NEW
Docket No.: 10035300180

FIG.9

FIG.10